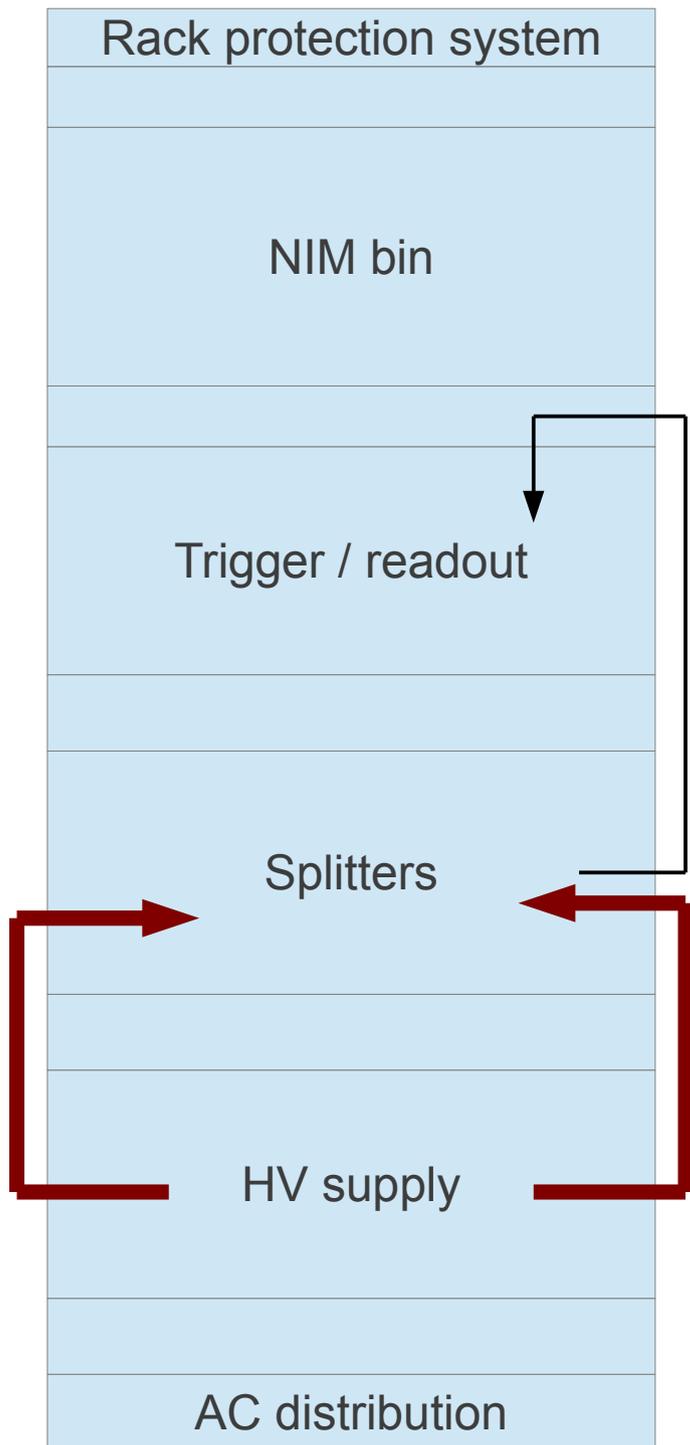


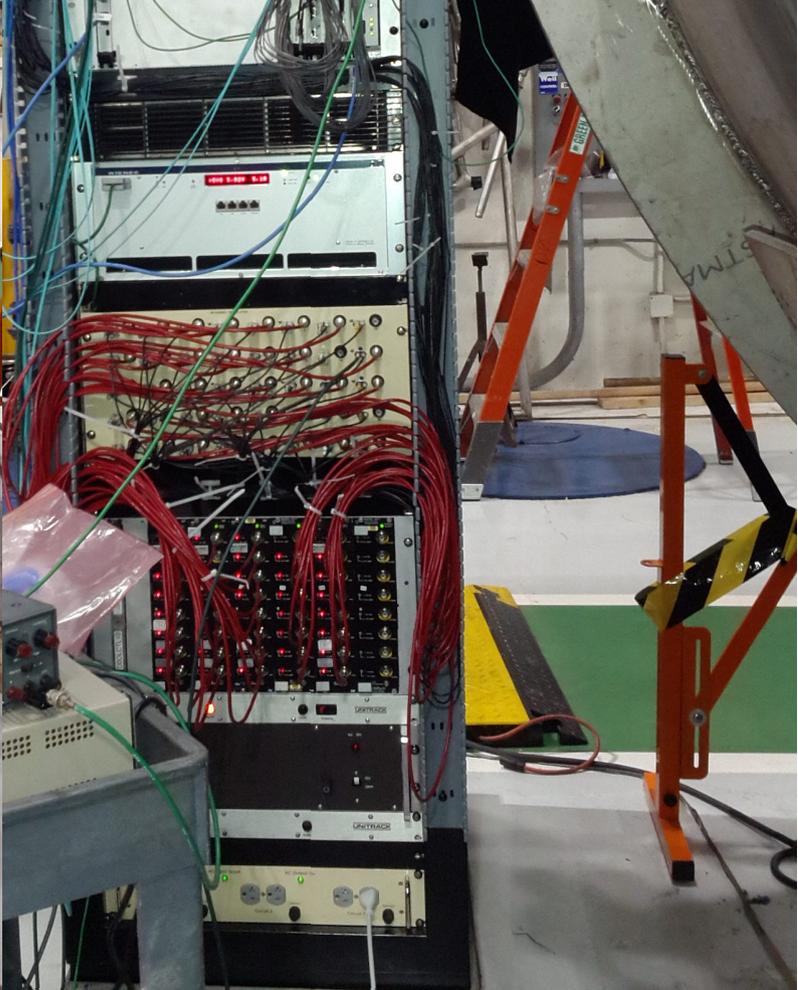
PMT pre-commissioning tests

Gabriel Collin



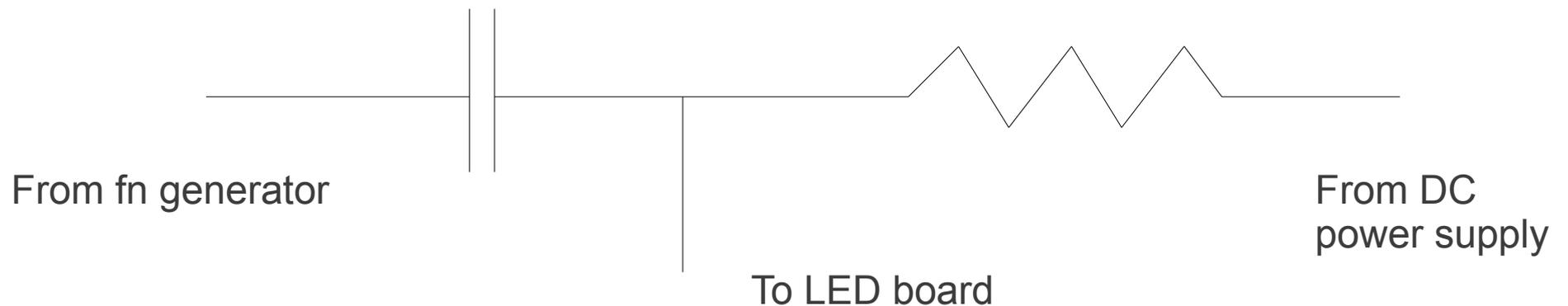
The rack

- Splitter chassis was moved from planned position (at the bottom of the rack) to just above the HV supply.
- This was made to shorten the path between the splitters and HV supply modules.



Flasher system

- Flasher mapping checked by Ben/Thomas.
- LEDs were driven using a similar system to the PMT crosstalk tests.
- A pulse provided by a function generator was joined with a DC offset and delivered to the LED board.



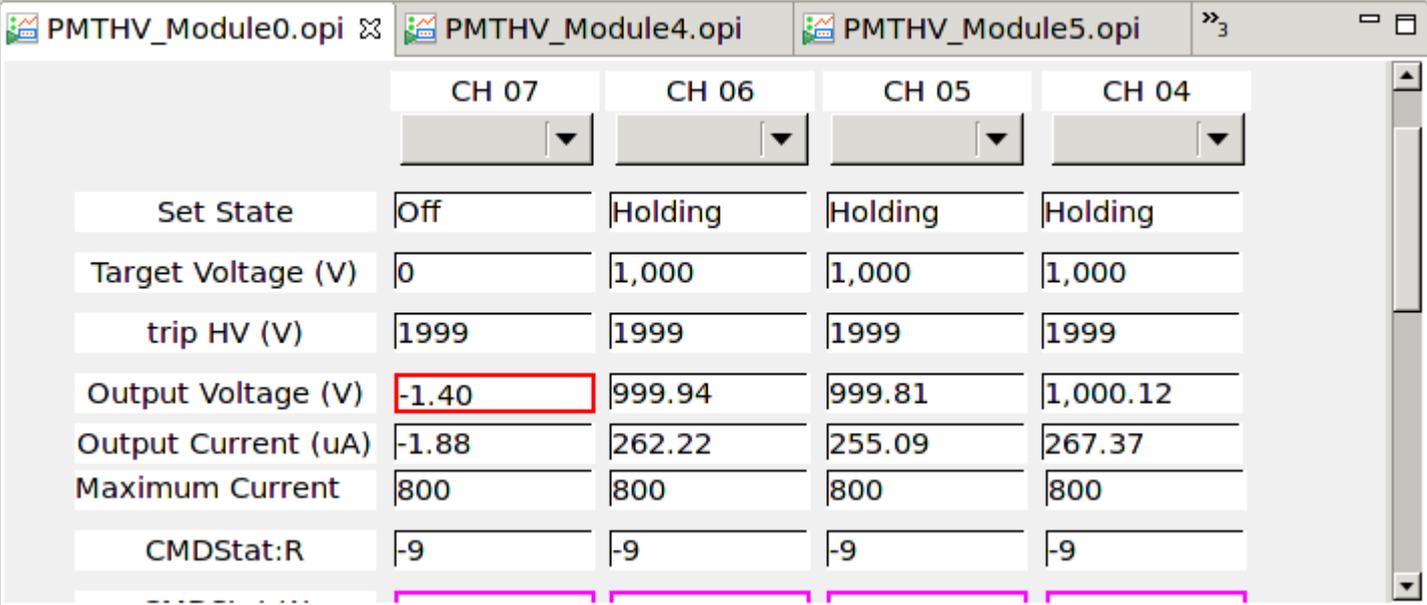
Connection to feed-through

- RG58 cables were scavenged for connecting the splitters to the feed-through.
- Before use, cables were tested for
 - Damaged connectors
 - Exposed shield braid
 - Connectivity under mechanical stress
 - Connectivity under HV (~1995 Volts)



HV supply

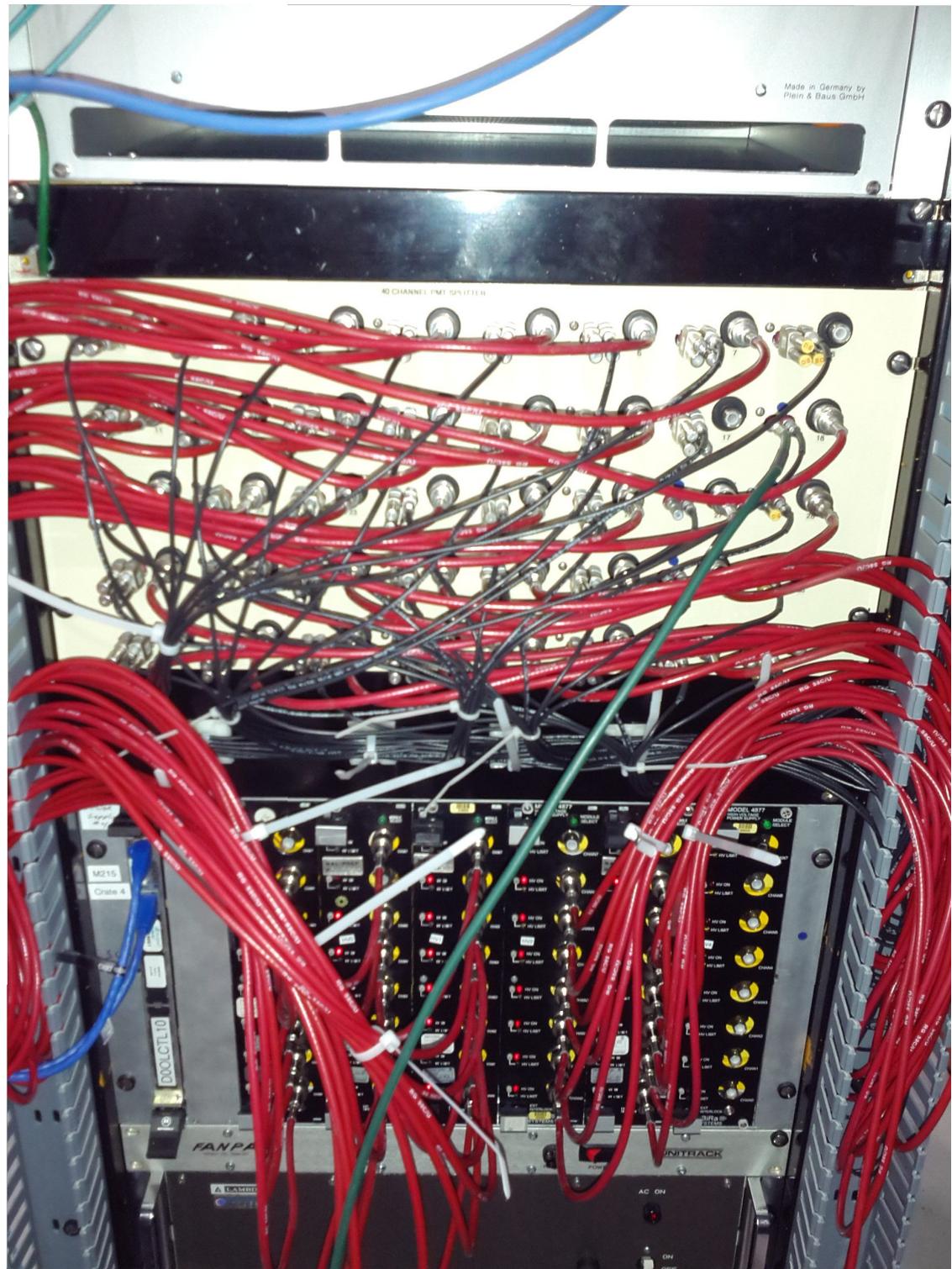
- Sowjanya sorted out the slow control GUI for the HV supply modules.
- Also found a few blown fuses on some of the motherboards and had them replaced.
- Motherboards require an interlock signal to operate.
 - This is now provided through a port on the back plane.



	CH 07	CH 06	CH 05	CH 04
Set State	Off	Holding	Holding	Holding
Target Voltage (V)	0	1,000	1,000	1,000
trip HV (V)	1999	1999	1999	1999
Output Voltage (V)	-1.40	999.94	999.81	1,000.12
Output Current (uA)	-1.88	262.22	255.09	267.37
Maximum Current	800	800	800	800
CMDStat:R	-9	-9	-9	-9

Cable routing

- Current routing situation is temporary.
 - Need to involve Linda for the final routing configuration.



Mode of operation

