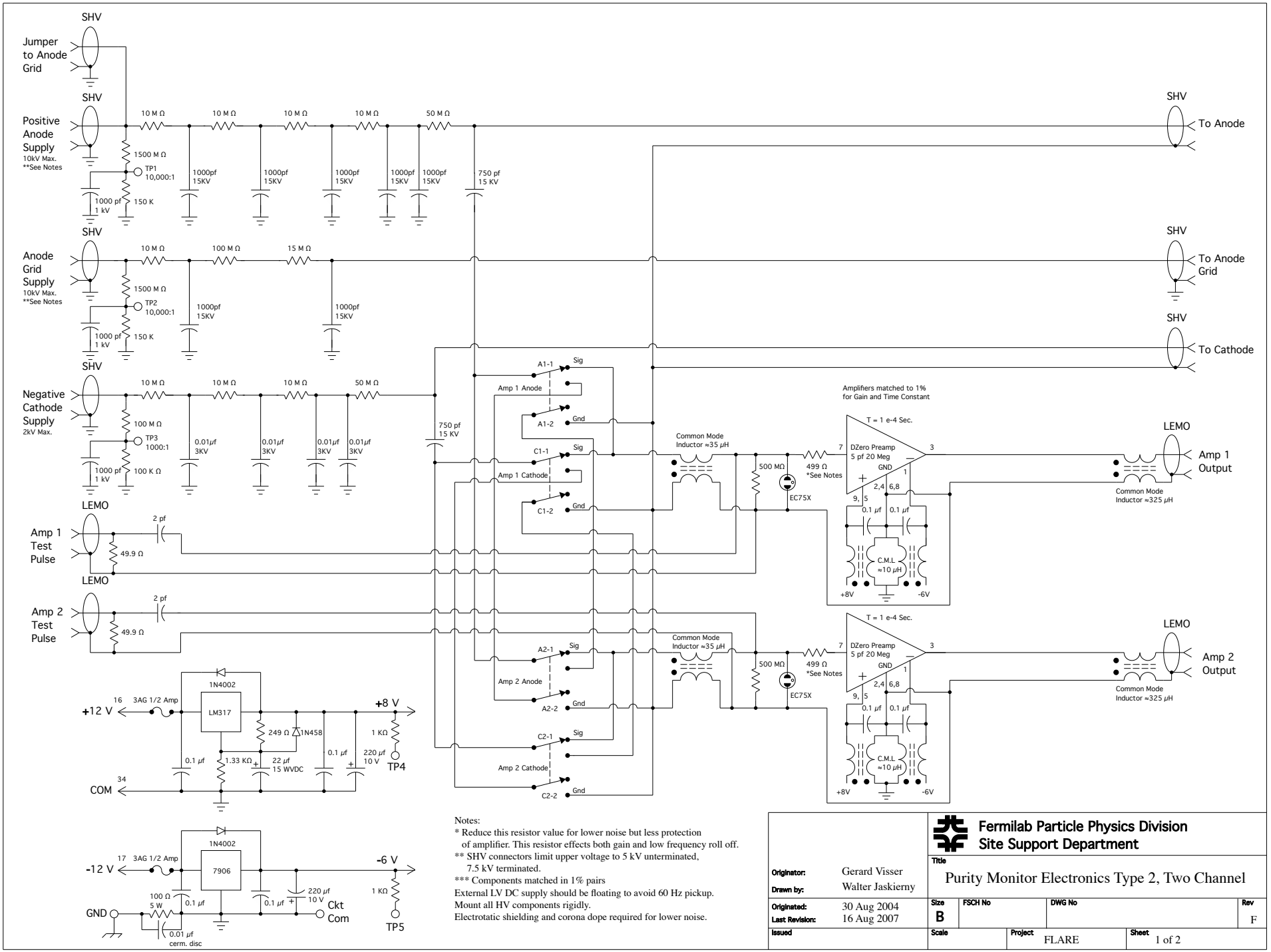

Notes:
* Reduce this resistor value for lower noise but less protection of amplifier. This resistor effects both gain and low frequency roll off.
** SHV connectors limit upper voltage to 5 kV unterminated, 7.5 kV terminated.
*** Components matched in 1% pairs
External LV DC supply should be floating to avoid 60 Hz pickup.
Mount all HV components rigidly.
Electrotatic shielding and corona dope required for lower noise.

Originator:	Gerard Visser	Fermilab Particle Physics Division Site Support Department			
Drawn by:	Walter Jaskierny	Title Purity Monitor Electronics Type 2, Two Channel			
Originated:	30 Aug 2004	Size B	FSCH No	DWG No	Rev F
Last Revision:	16 Aug 2007				
Issued		Scale	Project FLARE	Sheet 1 of 2	